

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Err r Definition
1	BRS	L1	1220	piezoelectric and (expan\$5 and contract\$4) and electrode\$1 and bias\$	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_TD	2004/05/25 09:30		
2	BRS	L2	56	piezoelectric and (expan\$5 and contract\$4) and electrode\$1 and voltage adj1 bias\$	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_TD	2004/05/25 09:45		
3	BRS	L3	1	6278625.pn. and voltage adj1 bias\$4	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_TD	2004/05/25 09:35		
4	BRS	L4	1	piezoelectric and (expan\$5 and contract\$4) same simultaneous and electrode\$1 and voltage adj1 bias\$	USPAT; US-PGP UB; EPO; JPO; DERWE NT; IBM_TD	2004/05/25 09:46		

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Applicant, Title of invention, Abstract — e.g. computer semiconductor

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piezoelectric expansion

AND ▼

AND

contraction

AND ▼

AND

voltage bias

AND ▼

AND

Date of publication of application — e.g. 19980401 - 19980405 -

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